

MAXIM

High-Precision, Low-Voltage, Micropower Operational Amplifier

MAX480

General Description

Maxim's MAX480 is a precision micropower operational amplifier with flexible power-supply capability and superior DC performance characteristics over the industry-standard OP90. The MAX480's guaranteed 140 μ V maximum offset voltage (25 μ V typ) is the lowest of any other micropower op amp. This represents an improvement over the highest grade OP90A. Similarly, input bias current, input offset current, and drift specifications are improved over the OP90 family.

Both input and output voltage ranges include the negative supply rail, allowing maximum signal range capability in single-supply applications. The MAX480 operates with either single supplies ranging from +1.6V to +36V or dual supplies from ± 0.8 V to ± 18 V. The MAX480 consumes less than 20 μ A, allowing operation in excess of 10,000 hours from a 250mA-hr lithium coin cell. Even with a minimal quiescent current, the amplifier sinks or sources 5mA from its output.

MAX480 is available in 8-pin DIP and Narrow Small Outline (SO) packages in commercial, extended, and military temperature ranges.

Applications

Precision Micropower Amplifiers
Micropower Signal Processing
Battery-Powered Analog Circuits

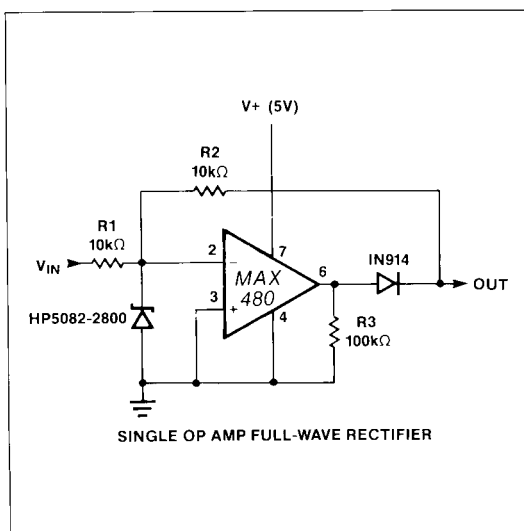
Features

- ◆ Single- or Dual-Supply Operation: +1.6V to +36V, ± 0.8 V to ± 18 V
- ◆ True Single-Supply Operation: Input and Output Voltage Ranges Include Ground
- ◆ 2.0 μ V/ $^{\circ}$ C Max Offset Voltage Drift
- ◆ 20 μ A Max Supply Current
- ◆ 5mA Min Output Drive
- ◆ 140 μ V Max Input Offset Voltage
- ◆ 3nA Max Input Bias Current
- ◆ 700V/mV Min Open-Loop Gain
- ◆ Standard 741 Pin Out With Nulling to V-
- ◆ Improved OP90 Replacement

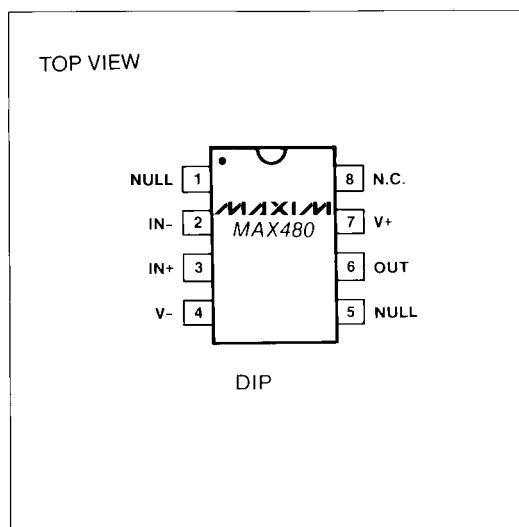
Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX480CPA	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 Plastic DIP
MAX480CSA	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 Narrow SO
MAX480EPA	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 Plastic DIP
MAX480ESA	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 Narrow SO
MAX480MJA	-55 $^{\circ}$ C to +125 $^{\circ}$ C	8 CERDIP

Typical Operating Circuit



Pin Configuration



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For free samples & the latest literature: <http://www.maxim-ic.com>, or phone 1-800-998-8800

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ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage (V_+ to V_-) $\pm 18V$
 Differential Input Voltage $[(V_-)-20V]$ to $[(V_+)+20V]$
 Common-Mode Input Voltage $[(V_-)-20V]$ to $[(V_+)+20V]$
 Output Short-Circuit Duration Indefinite
 Continuous Power Dissipation
 CERDIP (J)—derate at $8.0mW/^{\circ}C$ above $+70^{\circ}C$ $640mW$
 Plastic DIP (P)—derate at $9.09mW/^{\circ}C$ above $+70^{\circ}C$ $727mW$
 Small Outline (S)—derate at $5.88mW/^{\circ}C$ above $+70^{\circ}C$ $471mW$

Operating Temperature Ranges:

MAX480 (CPA, CSA) $0^{\circ}C$ to $+70^{\circ}C$
 MAX480 (EPA, ESA) $-40^{\circ}C$ to $+85^{\circ}C$
 MAX480MJA $-55^{\circ}C$ to $+125^{\circ}C$
 Junction Temperature (T_J) $-65^{\circ}C$ to $+150^{\circ}C$
 Storage Temperature Range $-65^{\circ}C$ to $+150^{\circ}C$
 Lead Temperature (Soldering, 10sec) $+300^{\circ}C$

Note 1: Absolute maximum ratings apply to packaged parts, unless otherwise noted.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

($V_S = \pm 1.5V$ to $\pm 15V$, $T_A = +25^{\circ}C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	V_{OS}			25	140	μV
Input Offset Current	I_{OS}	$V_{CM} = 0V$		0.2	1	nA
Input Bias Current	I_B	$V_{CM} = 0V$		1	3	nA
Large Signal Voltage Gain	A_{VO}	$V_S = \pm 15V$, $V_O = \pm 10V$ $R_L = 100k\Omega$	700	1200		V/mV
		$R_L = 10k\Omega$	300	600		
		$R_L = 2k\Omega$	100	250		
		$V_+ = 5V$, $V_- = 0V$, $1V < V_O < 4V$ $R_L = 100k\Omega$ $R_L = 10k\Omega$	200 100	400 180		
Input Voltage	IVR	$V_+ = 5V$, $V_- = 0V$ $V_S = \pm 15V$ (Note 2)	0/4 -15/13.5			V
Output Voltage Swing	V_O	$V_S = \pm 15V$ $R_L = 10k\Omega$ $R_L = 2k\Omega$	± 14 ± 11	± 14.2 ± 12		V
	V_{OH}	$V_+ = 5V$, $V_- = 0V$, $R_L = 2k\Omega$	4.0	4.2		
	V_{OL}	$V_+ = 5V$, $V_- = 0V$, $R_L = 10k\Omega$		100	500	μV
Common-Mode Rejection Ratio	CMRR	$V_+ = 5V$, $V_- = 0V$, $0V < V_{CM} < 4V$ $V_S = \pm 15V$, $-15V < V_{CM} < 13.5V$	90 100	110 130		dB
Power-Supply Rejection Ratio	PSRR			1.0	5.6	$\mu V/V$
Slew Rate	SR	$V_S = \pm 15V$	5	12		V/ms
Supply Current	I_{SY}	$V_S = \pm 1.5V$		9	15	μA
		$V_S = \pm 15V$		14	20	
Capacitive Load Stability		$A_V = +1$, No Oscillations (Note 3)	250	650		pF
Input Noise Voltage	e_{np-p}	$f_O = 0.1Hz$ to $10Hz$, $V_S = \pm 15V$		3		μV_{p-p}
Input Resistance Differential Mode	R_{IN}	$V_S = \pm 15V$		30		M Ω
Input Resistance Common Mode	R_{INCM}	$V_S = \pm 15V$		20		G Ω

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ELECTRICAL CHARACTERISTICS

($V_S = \pm 1.5V$ to $\pm 15V$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.)

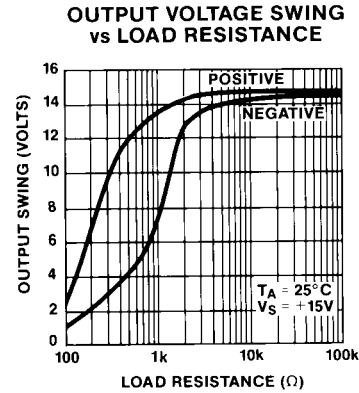
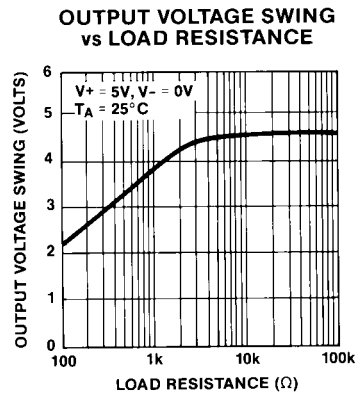
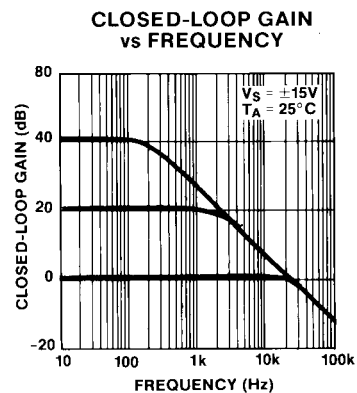
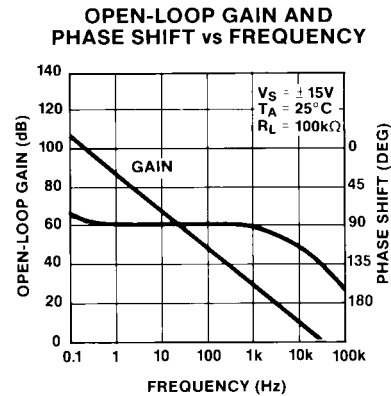
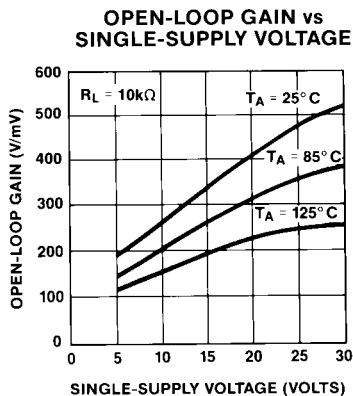
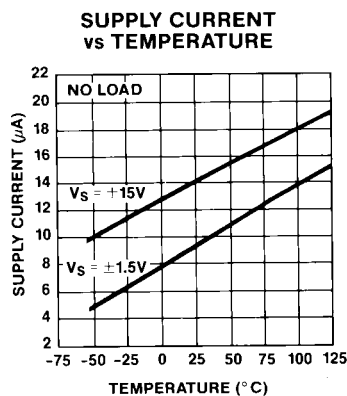
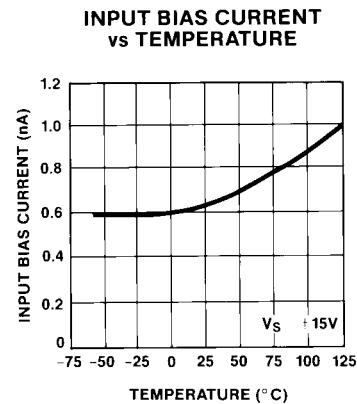
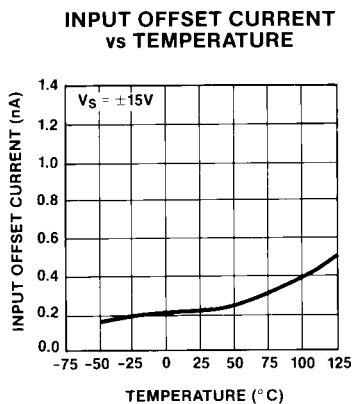
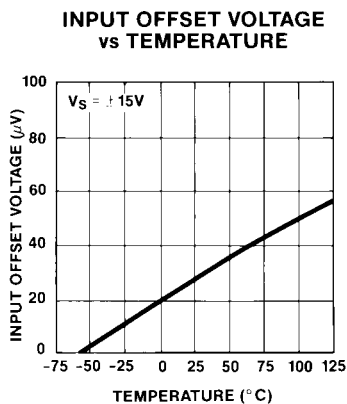
PARAMETER	SYMBOL	CONDITIONS	MAX480C			MAX480E			MAX480M			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage	V_{OS}		40	220		50	250		70	300		μV
Input Offset Voltage Drift	TCV_{OS}	(Note 3)	0.3	2.0		0.3	2.0		0.3	2.0		$\mu V/^{\circ}C$
Input Offset Current	I_{OS}	$V_{CM} = 0V$	0.2	2.0		0.3	2.0		0.5	2.0		nA
Input Bias Current	I_B	$V_{CM} = 0V$	1	3		2	5		3	7		nA
Large Signal Voltage Gain	A_{VO}	$V_S = \pm 15V$, $V = \pm 10V$ $R_L = 100k\Omega$	500	950		500	800		180	400		V/mV
		$R_L = 10k\Omega$ $R_L = 2k\Omega$	200 75	400 125		200 75	400 150		100 50	240 110		
		$V+ = 5V$, $V- = 0V$, $1V < V_O < 4V$ $R_L = 100k\Omega$ $R_L = 10k\Omega$	150 75	360 150		150 75	280 140		75 50	200 110		V
Input Voltage Range	IVR	$V+ = 5V$, $V- = 0V$ $V_S = \pm 15V$ (Note 2)	0/3.5 -15/13.5			0/3.5 -15/13.5			0/3.5 -15/13.5			V
Output Voltage Swing	V_O	$V_S = \pm 15V$ $R_L = 10k\Omega$ $R_L = 2k\Omega$	± 13.5 ± 10.5	± 14 ± 11.8		± 13.5 ± 10.5	± 14 ± 11.8		± 13.5 ± 10.5	± 13.7 ± 11.5		V
	V_{OH}	$V+ = 5V$, $V- = 0V$ $R_L = 2k\Omega$	3.9	4.1		3.9	4.1		3.9	4.1		
	V_{OL}	$V+ = 5V$, $V- = 0V$ $R_L = 10k\Omega$		100	500		100	500		100	500	μV
Common-Mode Rejection Ratio	CMRR	$V+ = 5V$, $V- = 0V$, $0V < V_{CM} < 3.5V$	90	110		90	110		85	105		dB
		$V_S = \pm 15V$, $-15V < V_{CM} < 13.5V$	100	120		100	120		95	115		
Power-Supply Rejection Ratio	PSRR		1.0	5.6		1.0	5.6		3.2	10		$\mu V/V$
Supply Current	I_{SY}	$V_S = \pm 1.5V$	12	25		13	25		15	25		μA
		$V_S = \pm 15V$	16	30		17	30		19	30		

Note 2: Guaranteed by CMRR test.

Note 3: Guaranteed by design.

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Typical Operating Characteristics

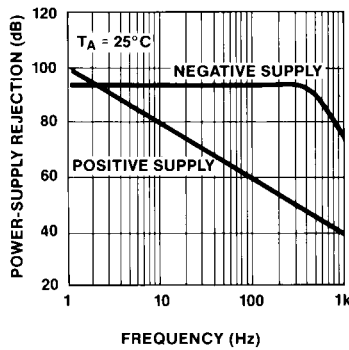


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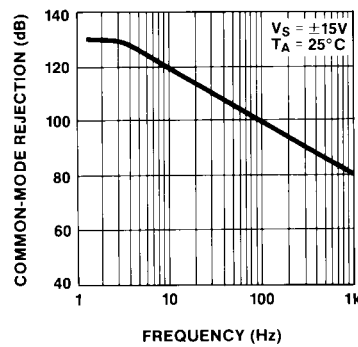
Typical Operating Characteristics (continued)

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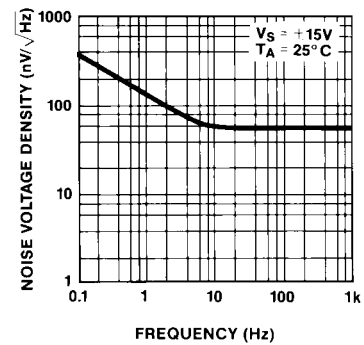
POWER-SUPPLY REJECTION
RATIO vs FREQUENCY



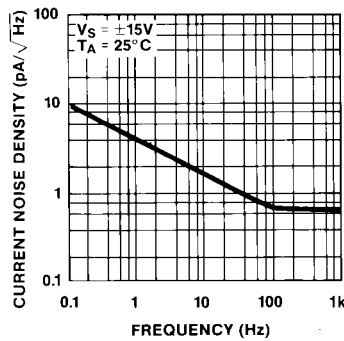
COMMON-MODE REJECTION
RATIO vs FREQUENCY



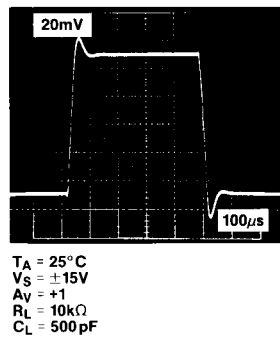
NOISE VOLTAGE DENSITY
vs FREQUENCY



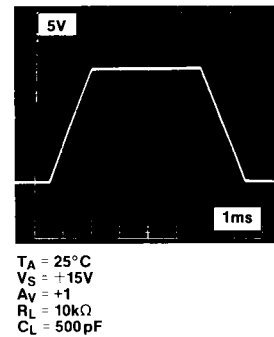
CURRENT NOISE DENSITY
vs FREQUENCY



SMALL SIGNAL
TRANSIENT RESPONSE



LARGE SIGNAL
TRANSIENT RESPONSE



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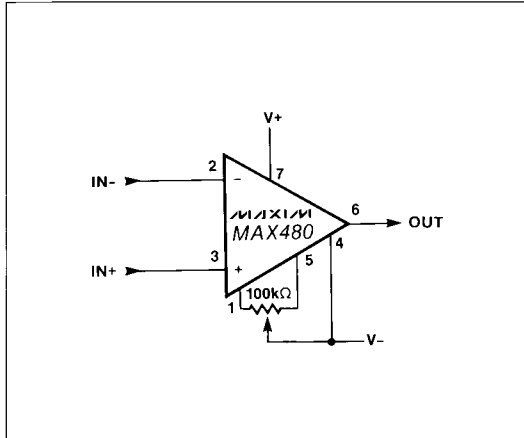


Figure 1. Offset Nulling Circuit

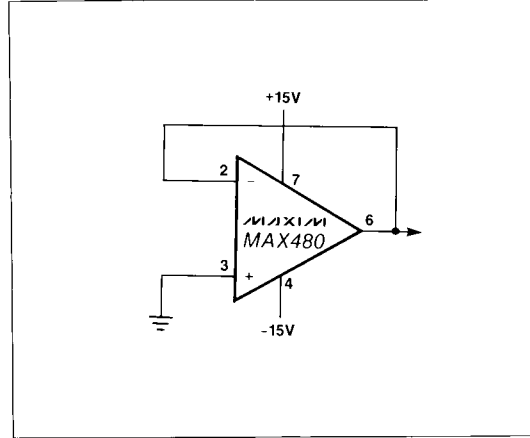
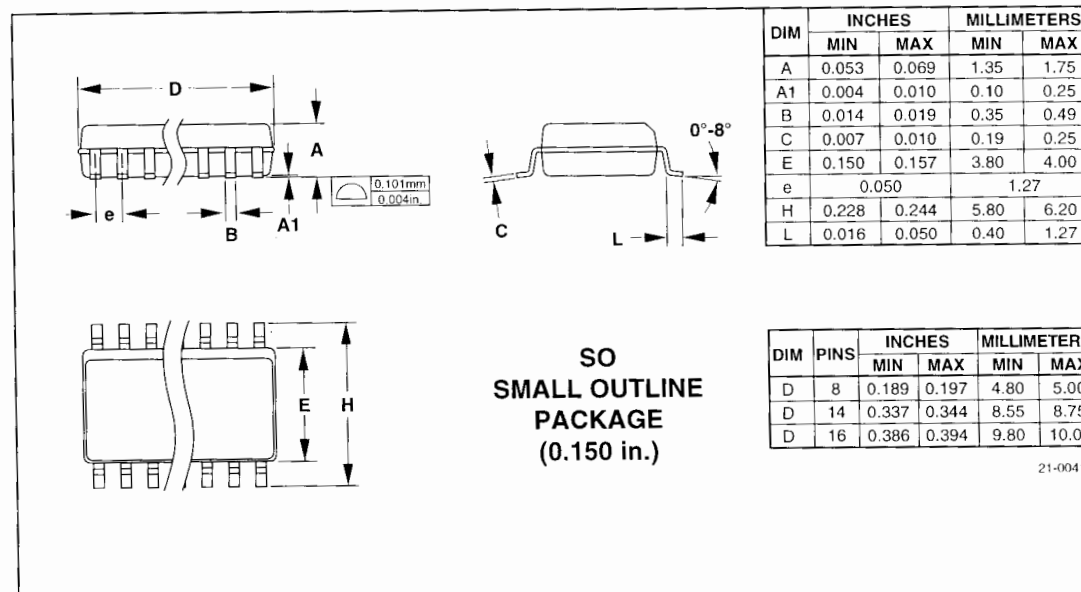
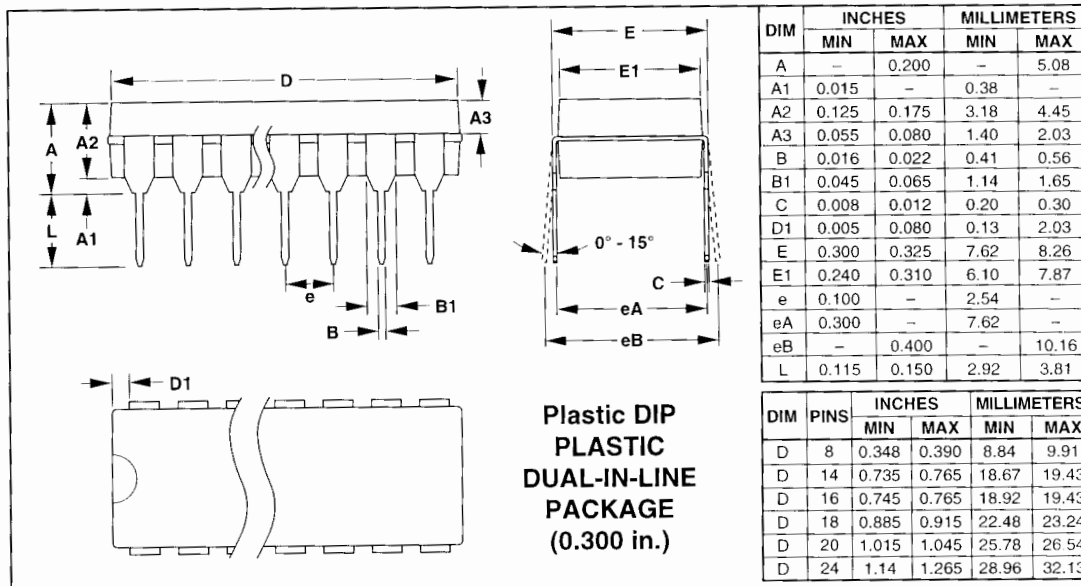


Figure 2. Burn-In Circuit

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Package Information

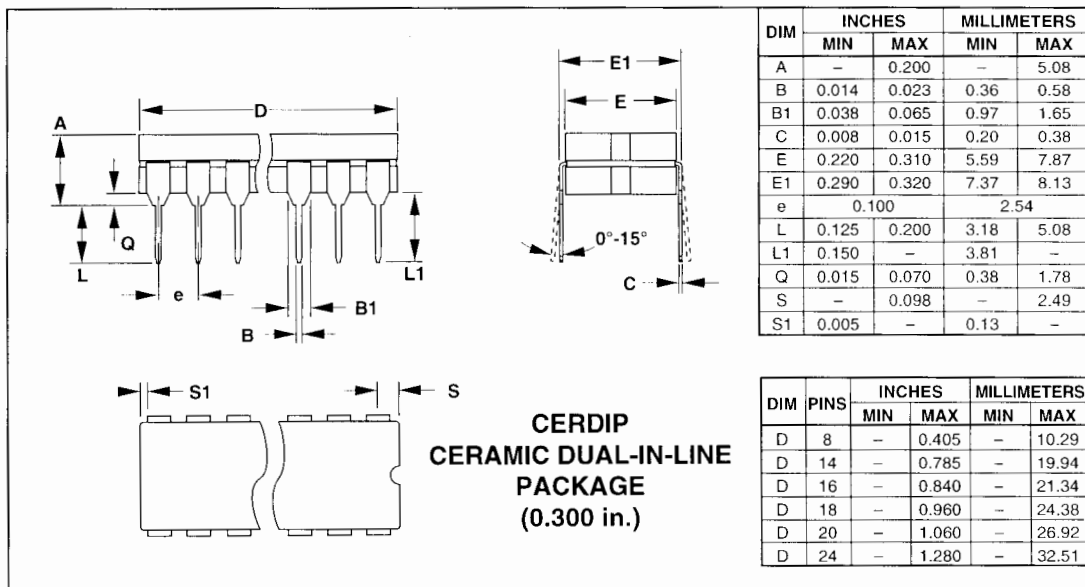
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Package Information (continued)



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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